

L Number	Hits	Search Text	DB	Time stamp
-	6	two adj1 stage\$1 adj1 equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 08:29
-	7	((two or more or multi\$3) with stage\$1) with equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 11:55
-	6	((two or more or multiple) adj1 stage\$1) with equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 11:49
-	7	((two or more or multi\$3) adj1 stage\$1) with equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 11:50
-	20	((two or more or multi\$3) adj1 stage\$1) with equalizer and trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 11:50
-	1		USPAT	2004/09/22 11:55
-	3	(forward and decision adj1 feedback) with equalizer same trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 16:07
-	155	(forward and decision adj1 feedback) with equalizer and trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:36
-	7	(first and second) with (forward and decision adj1 feedback) with equalizer and trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:58
-	19	"5692011"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:15
-	25	"5539774"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:30
-	6	"6418164"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:31
-	6	"6411659"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:33
-	13	"6307901"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:35
-	16	"6253345"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:37
-	7	"6246723"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:38
-	8	"6201832"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:39
-	6	"6035428"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:41

	22	"5572262"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:44
	28	"5513215"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:48
	13	"5453797"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:50
	35	"5414738"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:51
	1	((first and second) or dual) with forward adj1 equalizer and decision adj1 feedback adj1 equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 14:59
	11	forward adj1 equalizer and decision adj1 feedback adj1 equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:05
	38	((forward adj1 equalizer) or \$1FE) and ((decision adj1 feedback adj1 equalizer) or DFE) with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:07
	6	((two or dual or multiple or multi) adj1 stage\$1) adj1 equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:16
	6	((two or dual) adj1 stage\$1) adj1 equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:16
	7	((two or dual or multiple or multi) adj1 stage\$1) with equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:22
	18	(dual or more or multi\$3 or serial) with equalizer with trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:40
	20	((two or dual or multiple or multi) adj1 stage\$1) with equalizer and trellis adj1 cod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:38
	204	((two or dual or multiple or multi) adj1 stage\$1) with equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:23
	3	((two or dual) adj1 stage) or multi\$1 adj1 stage\$1) adj1 decision adj1 feedback adj1 equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:26
	11	((two or dual) adj1 stage) or multi\$1 stage\$1) adj1 decision adj1 feedback adj1 equalizer	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:26
	19	((two or dual) adj1 stage) or multi\$1 adj1 stage\$1) with (decision adj1 feedback adj1 equalizer)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:29
	8	(forward adj1 equalizer) with decision adj1 feedback adj1 equalizer with trellis adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:44
	12	((two or dual or multiple or multi) adj1 stage\$1) with equalizer and trellis adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 16:07

-	11	(forward adj1 equalizer) with decision adj1 feedback adj1 equalizer same trellis adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:40
-	65	(dual or more or multi\$3 or serial) with equalizer with trellis adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 07:22
-	11	(forward adj1 equalizer) with (decision adj1 feedback adj1 equalizer) same (trellis adj1 decod\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:46
-	27	(forward adj1 equalizer) with (decision adj1 feedback adj1 equalizer) and (trellis adj1 decod\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:47
-	67	(forward adj1 equalizer) with (decision adj1 feedback adj1 equalizer) and (trellis or viterbi or TCM)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:48
-	14	(forward adj1 equalizer) with (decision adj1 feedback adj1 equalizer) with (trellis or viterbi or TCM)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:47
-	56	(forward adj1 equalizer) with (decision adj1 feedback adj1 equalizer) and (trellis)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 15:48
-	7	((two or dual or multiple or multi) adj1 stage\$1) with equalizer with (trellis or viterbi or TCM) adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 07:49
-	56	(forward and decision adj1 feedback) with equalizer with (trellis or viterbi or TCM) adj1 (cod\$3 or decod\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/22 16:10
-	65	(dual or more or multi\$3 or serial) with (equalizer with trellis adj1 decod\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 07:22
-	21	"5546430"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 08:11
-	12	"6240133"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 08:11
-	10	(birru with dagnachew or philips) and (forward adj1 equalizer with decision adj1 feedback adj1 equalizer) and trellis adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 08:33
-	50	(birru with dagnachew or philips) and equalizer and trellis adj1 decod\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/23 08:33



RELEASE 1.8

Welcome
United States Patent and Trademark OfficeIEEE Xplore®
1 Million Documents
1 Million Users

» Search Results

Help FAQ Terms IEEE Peer Review

Quick Links

Welcome to IEEE Xplore®

- Home
- What Can I Access?
- Log-out

Tables of Contents

- Journals & Magazines
- Conference Proceedings
- Standards

Search

- By Author
- Basic
- Advanced

MEMBER SERVICES

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

IEEE Xplore®

- Access the IEEE Enterprise File Cabinet

Print Format

Welcome
United States Patent and Trademark OfficeYour search matched **19** of **1074479** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance in Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

forward equalizer and decision feedback equalizer

Search

 Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard**1 An adaptive analog noise-predictive decision-feedback equalizer**

Le, M.Q.; Hurst, P.J.; Keane, J.P.;
Solid-State Circuits, IEEE Journal of, Volume: 37, Issue: 2, Feb. 2002
Pages:105 - 113

[\[Abstract\]](#) [\[PDF Full-Text \(180 KB\)\]](#) IEEE JNL**2 Continuous-time forward equalization for the decision-feedback-equalizer-based read channel**

Brown, J.E.C.; Hurst, P.J.;
Magnetics, IEEE Transactions on, Volume: 34, Issue: 4, July 1998
Pages:2372 - 2381

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) IEEE JNL**3 An adaptive analog noise-predictive decision-feedback equalizer**

Le, M.Q.; Hurst, P.J.; Keane, J.P.;
VLSI Circuits, 2000. Digest of Technical Papers. 2000 Symposium on, 15-17 June 2000
Pages:216 - 217

[\[Abstract\]](#) [\[PDF Full-Text \(260 KB\)\]](#) IEEE CNF**4 An adaptive noise-predictive decision-feedback equalizer for the magnetic recording channel**

Le, M.Q.; Hurst, P.J.; Wang, X.;
Communications, Computers and Signal Processing, 1999 IEEE Pacific Rim Conference on, 22-24 Aug. 1999
Pages:560 - 563

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) IEEE CNF**5 A 100 MHz, 5MBaud QAM decision-feedback equalizer for digital television applications**

Joshi, R.B.; Samueli, H.;
Solid-State Circuits Conference, 1994. Digest of Technical Papers. 41st ISSCC.,
1994 IEEE International , 16-18 Feb. 1994
Pages:68 - 69

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) [IEEE CNF](#)

6 A comparison of analog DFE architectures for disk-drive applications
Brown, J.E.C.; Hurst, P.J.; Der, L.; Agi, I.;
Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium
on , Volume: 4 , 30 May-2 June 1994
Pages:99 - 102 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(324 KB\)\]](#) [IEEE CNF](#)

**7 Performance of electrical equalizers in optically amplified OOK and DPSK
systems**

Jin Wang; Kahn, J.M.;
Photonics Technology Letters, IEEE , Volume: 16 , Issue: 5 , May 2004
Pages:1397 - 1399

[\[Abstract\]](#) [\[PDF Full-Text \(152 KB\)\]](#) [IEEE JNL](#)

8 Adaptive PMD compensation by electrical and optical techniques

Buchali, F.; Henning Bulow;
Lightwave Technology, Journal of , Volume: 22 , Issue: 4 , April 2004
Pages:1116 - 1126

[\[Abstract\]](#) [\[PDF Full-Text \(512 KB\)\]](#) [IEEE JNL](#)

**9 Mostly analog disk drive read channel with practical depth-of-two fixed
delay tree search**

Wei, D.C.; Sun, D.Q.; Abidi, A.A.;
Magnetics, IEEE Transactions on , Volume: 38 , Issue: 6 , Nov. 2002
Pages:3689 - 3698

[\[Abstract\]](#) [\[PDF Full-Text \(1001 KB\)\]](#) [IEEE JNL](#)

**10 A CMOS adaptive continuous-time forward equalizer, LPF, and RAM-DFE
for magnetic recording**

Brown, J.E.C.; Hurst, P.J.; Rothenberg, B.C.; Lewis, S.H.;
Solid-State Circuits, IEEE Journal of , Volume: 34 , Issue: 2 , Feb. 1999
Pages:162 - 169

[\[Abstract\]](#) [\[PDF Full-Text \(364 KB\)\]](#) [IEEE JNL](#)

**11 A comparison of two blind equalization algorithms for broadband indoor
wireless communications**

Lin He; Malkemes, R.; Reed, C., Jr.; Amin, M.G.;
Signal Processing and its Applications, Sixth International, Symposium on.
2001 , Volume: 2 , 13-16 Aug. 2001
Pages:505 - 508 vol.2.

[\[Abstract\]](#) [\[PDF Full-Text \(280 KB\)\]](#) [IEEE CNF](#)

12 Adaptive equalization for 100 Mbps OWSS wireless LANs
Dholakia, J.H.; Jain, V.K.; Myers, B.A.;

Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE , Volume: 6 , 25-29 Nov. 2001
Pages:3583 - 3587 vol.6

[\[Abstract\]](#) [\[PDF Full-Text \(284 KB\)\]](#) [IEEE CNF](#)

13 Adaptive equalizers for lapped multitone systems

Neurohr, N.;
Signals, Systems, and Computers, 1999. Conference Record of the Thirty-Third Asilomar Conference on , Volume: 2 , 24-27 Oct. 1999
Pages:917 - 921 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) [IEEE CNF](#)

14 Design of multilevel decision feedback equalizers

Mathew, G.; Farhang-Boroujeny, B.; Wood, R.W.;
Magnetics, IEEE Transactions on , Volume: 33 , Issue: 6 , Nov. 1997
Pages:4528 - 4542

[\[Abstract\]](#) [\[PDF Full-Text \(480 KB\)\]](#) [IEEE JNL](#)

15 A blind adaptation algorithm for decision feedback equalizers with fractionally-spaced feedforward filters

Zukunft, R.; Haar, S.; Magesacher, T.;
Digital Signal Processing, 2002. DSP 2002. 2002 14th International Conference on , Volume: 2 , 1-3 July 2002
Pages:779 - 782 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) [IEEE CNF](#)

[1](#) [2](#) [Next](#)

Welcome to IEEE Xplore®

- Home
- What Can I Access?
- Log-out

Tables of Contents

- Journals & Magazines
- Conference Proceedings
- Standards

Search

- By Author
- Basic
- Advanced

MEMBER SERVICES

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

IEEE EXPERTISE

- Access the IEEE Enterprise File Cabinet

 Print FormatYour search matched **9** documents.

A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Results Key:**JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 A newly proposed ATSC DTV system for transmitting a robust bit-stream along with the standard bit-stream***Gaddam, V.R.; Birru, D.;*Consumer Electronics, IEEE Transactions on , Volume: 49 , Issue: 4 , Nov. 2003
Pages:933 - 938[\[Abstract\]](#) [\[PDF Full-Text \(384KB\)\]](#) **IEEE JNL****2 Recursive bitstream conversion: third-order structures***Roza, E.; Birru, D.;*Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on] , Volume: 49 , Issue: 5 , May 2002
Pages:591 - 601[\[Abstract\]](#) [\[PDF Full-Text \(495KB\)\]](#) **IEEE JNL****3 A generalized multirate topology of reduced-sample-rate $\Sigma\Delta$ modulators with optimum coefficients***Birru, D.;*Signal Processing Letters, IEEE , Volume: 6 , Issue: 8 , Aug. 1999
Pages:196 - 198[\[Abstract\]](#) [\[PDF Full-Text \(152KB\)\]](#) **IEEE JNL****4 A novel delay-locked loop based CMOS clock multiplier***Birru, D.;*Consumer Electronics, IEEE Transactions on , Volume: 44 , Issue: 4 , Nov. 1998
Pages:1319 - 1322[\[Abstract\]](#) [\[PDF Full-Text \(432KB\)\]](#) **IEEE JNL****5 Optimized reduced sample rate sigma-delta modulation***Birru, D.;*Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Volume: 44 , Issue: 11 , Nov. 1997
Pages:896 - 906[\[Abstract\]](#) [\[PDF Full-Text \(392KB\)\]](#) **IEEE JNL**

6 Noise shaping with reduced clock frequency*Birru, D.;*

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World',, 1996 IEEE International Symposium on ,Volume: 3 , 12-15 May 1996

Pages:233 - 236 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(360KB\)\]](#) [IEEE CNF](#)

7 A newly proposed ATSC DTV system for transmitting a robust bit-stream along with the standard bit-stream*Gaddam, V.R.; Birru, D.;*

Consumer Electronics, 2003. ICCE. 2003 IEEE International Conference on , 17-19 June 2003

Pages:122 - 123

[\[Abstract\]](#) [\[PDF Full-Text \(231KB\)\]](#) [IEEE CNF](#)

8 Video-rate D/A converter using reduced rate sigma-delta modulation*Birru, D.; Roza, E.;*

Custom Integrated Circuits Conference, 1998., Proceedings of the IEEE 1998 , 11-14 May 1998

Pages:241 - 244

[\[Abstract\]](#) [\[PDF Full-Text \(476KB\)\]](#) [IEEE CNF](#)

9 Use of comb filters for efficient VLSI implementation of reduced clock frequency noise shapers*Birru, D.;*

Electronics Letters ,Volume: 32 , Issue: 14 , 4 July 1996

Pages:1268 - 1270

[\[Abstract\]](#) [\[PDF Full-Text \(316KB\)\]](#) [IEE JNL](#)